

## Refine Search

### Search Results -

Terms	Documents
(361/683  361/684  361/685  361/686  322/32  709/233  370/257  710/33  710/300  710/307  710/58  710/240  710/309  710/15  710/60  710/313  340/825  713/600  713/501  713/320  713/322).ccls.	17352

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
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 Derwent World Patents Index  
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Search:

L7

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### Search History

DATE: Wednesday, October 31, 2007    [Purge Queries](#)    [Printable Copy](#)    [Create Case](#)

SetName Queryside by  
side

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

L7 710/33,300,307,58,240,309,15,60,313;713/600,501,320,322;340/825;370/257;709/233;322/32;36686.ccls.

L6 L1 same bus same (variable near3 speed)

L5 L1 same bus same speed

L4 L1 same bus

L3 L1 and "variable speed bus"

L2 L1 same "variable speed bus"

L1 (access\$3 near5 (request or rate)) same ((adjust\$3 or chang\$3 or determin\$3) near5 clock)

END OF SEARCH HISTORY

## Refine Search

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### Search Results -

Terms	Documents
L4 and L7	30

Database:

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 EPO Abstracts Database  
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Search:

L8



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Name Query  
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*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

L8    l4 and L7

L7    710/33,300,307,58,240,309,15,60,313;713/600,501,320,322;340/825;370/257;709/233;322/32;36  
 686.ccls.

L6    L1 same bus same (variable near3 speed)

L5    L1 same bus same speed

L4    L1 same bus

L3    L1 and "variable speed bus"

L2    L1 same "variable speed bus"

L1    (access\$3 near5 (request or rate)) same ((adjust\$3 or chang\$3 or determin\$3) near5 clock)

END OF SEARCH HISTORY

## Refine Search

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### Search Results -

Terms	Documents
L5 or L8	55

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Name Query  
 side by  
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*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

L9 15 or 18

L8 14 and L7

L7 710/33,300,307,58,240,309,15,60,313;713/600,501,320,322;340/825;370/257;709/233;322/32;36686.ccls.

L6 L1 same bus same (variable near3 speed)

L5 L1 same bus same speed

L4 L1 same bus

L3 L1 and "variable speed bus"

L2 L1 same "variable speed bus"

L1 (access\$3 near5 (request or rate)) same ((adjust\$3 or chang\$3 or determin\$3) near5 clock)

END OF SEARCH HISTORY

EAST - [Untitled1:1]

FileViewEditToolsWindowHelp

Drafts

Pending

Active

L1: (1) (access\$3 near5 (req

Failed

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Favorites

Tagged (0)

UDC

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Search

List

Browse

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Clear

DBs

USPAT

Default operator:

OR

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BRS form

IS&R form

Image

Text

HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error	Definition	Err
1	BRS	L1	1	(access\$3 near5 (request or rate)) same ((variable	USPAT	2007/10/31 15:33				

Start

EAST - [Un...

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts

Pending

Active

Failed

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Favorites

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UDC

Queue

Trash

Search

List

Browse

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DBs

USPAT

Default operator

OR

Plurals

Highlight all hit terms initially

(access\$3 near5 (request or rate)) same ((variable adj1 speed) near5 bus)

BRS form

IS&R form

Image

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6006303 A	19991221	17	Priority encoding and decoding for memory	710/244	710/240; 710/40;	


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Results for "(( clock&lt;in&gt;metadata ) &lt;and&gt; ( bus&lt;in&gt;metadata ) )&lt;and&gt; ( speed&lt;in&gt;me..."

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Your search matched **18** of **1678917** documents.A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

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(( clock&lt;in&gt;metadata ) &lt;and&gt; ( bus&lt;in&gt;metadata ) )&lt;and&gt; ( speed&lt;in&gt;metadata ) ar

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

## » Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

[Select All](#) [Deselect All](#)

- ☐ 1. **Scalable hardware priority queue architectures for high-speed packet swi**  
 Sung-Whan Moon; Rexford, J.; Shin, K.G.;  
[Computers, IEEE Transactions on](#)  
 Volume 49, [Issue 11](#), Nov. 2000 Page(s):1215 - 1227  
 Digital Object Identifier 10.1109/12.895938  
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- ☐ 2. **On the nature and inadequacies of transport timing delay constructs in V descriptions**  
 Walker, P.A.; Ghosh, S.;  
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 Volume 16, [Issue 8](#), Aug. 1997 Page(s):894 - 915  
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- ☐ 3. **A novel 2 GHz multi-layer AMBA high-speed bus interconnect matrix for :**  
 Landry, A.; Nekili, M.; Savaria, Y.;  
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 23-26 May 2005 Page(s):3343 - 3346 Vol. 4  
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- ☐ 4. **Eliminating non-determinism during test of high-speed source synchron buses**  
 Mohanram, K.; Touba, N.A.;  
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- ☐ 5. **A signalling system for multiprocessors**  
 Prete, C.A.; Rizzo, L.;  
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- ☐ 6. **A dynamic high speed multi-media local area network (MLAN) protocol at**  
Agrawal, J.P.; Varshney, U.;  
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11-14 Oct. 1992 Page(s):1011 - 1015 vol.3  
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- ☐ 7. **CADRE: Cycle-Accurate Deterministic Replay for Hardware Debugging**  
Sarangi, S.R.; Greskamp, B.; Torrellas, J.;  
[Dependable Systems and Networks, 2006. DSN 2006. International Conferenc](#)  
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- ☐ 8. **A mixed EFL I<sup>2</sup>L digital telecommunication integrated circuit**  
Baumert, R.J.; Cameron, L.E.; Wilson, R.A., III;  
[Electron Devices, IEEE Transactions on](#)  
Volume 31, [Issue 2](#), Feb 1984 Page(s):160 - 165  
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Baumert, R.J.; Cameron, L.E., III; Wilson, R.A.;  
[Solid-State Circuits, IEEE Journal of](#)  
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- ☐ 10. **A process-independent, 800-MB/s, DRAM byte-wide interface featuring c**  
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- ☐ 11. **An architecture and compiler for scalable on-chip communication**  
Jian Liang; Laffely, A.; Srinivasan, S.; Tessier, R.;  
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- ☐ 12. **High-Speed FPGA-Based Pulse-Height Analyzer for High Resolution X-R**  
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- ☐ **15. A Fault-Tolerant Dynamic Fetch Policy for SMT Processors in Multi-Bus**  
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